

HARDWARE CHARACTERISTICS		UNIT	
Input voltage		VDC	24 ± 10%
Power input		W	36
Weight		g	975

CONTROL FEATURES		UNIT	
Movement types			Synchronized and interpolated
Number of supported interpolation group	-		2
Max number of degrees of freedom (DOFs) per interpolation group	-		4 (X, Y, Z, Theta)
Embedded set point generator	-		G-code, PVT, PT
On board I/Os	-		8x RS-485 fast digital I/Os (software programmable, SPI ready)
TRANSNET real-time communication bus	-		1 Gbps
TRANSNET cycle time	µs		100
Number of supported axes on TRANSNET	-		Up to 63
Traces with pre-/post-trigger function	-		Yes
Master/Slave data exchange	kHz		10 (real-time including slave to slave communication capability)
User programmability	-		Customer Software Module (CSM) running internally as a FreeRTOS application
Language	-		C program linked to embedded EDI libraries
Operating system	-		FreeRTOS
Customer Development environment	-		Xilinx SDK
Software Module Number of dedicated cores	-		1 (ARM Cortex A53 at 1.3 GHz)
(CSM) Ethernet dedicated port	-		1x TCP/IP (1 Gbps)
Dedicated SD card	-		1x (max. 32 GB)
Debug port	-		JTAG and TCP/IP
Communication To PC	-		2x TCP/IP (up to 1 Gbps) and 1x cabled PCIe Gen1 (2.5 Gbps)
interface To AccurET controllers	-		using a x4 lane connector TRANSNET
Software / COMET commissioning software	-		For commissioning / monitoring (for software compatibility, refer to the COMET manual)
programmability ETEL Device Interface (EDI)	-		DLL files for C / C++ / .NET (for software compatibility, refer to the EDI manual)
from host PC Firmware update	-		Ethernet TCP/IP

